


## APPLICATION DATA SHEET

Electronic Version v14

Stylesheet Version v14.0

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|-------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------------|--|--|
| <b>Title of Invention</b>                                                           | METHOD FOR FABRICATING INTEGRATED CIRCUITS HAVING BOTH HIGH VOLTAGE AND LOW VOLTAGE DEVICES |  |  |
| Application Type : regular, utility<br>Attorney Docket Number : NAUP0575USA         |                                                                                             |  |  |
| Correspondence address:<br>Customer Number: 027765                                  |                                                                                             |  |  |
|  |                                                                                             |  |  |
| Inventors Information:                                                              |                                                                                             |  |  |
| <u>Inventor 1:</u>                                                                  |                                                                                             |  |  |
| <b>Applicant Authority Type:</b>                                                    | Inventor                                                                                    |  |  |
| <b>Citizenship:</b>                                                                 | TW                                                                                          |  |  |
| <b>Given Name:</b>                                                                  | Jung-Ching                                                                                  |  |  |
| <b>Family Name:</b>                                                                 | CHEN                                                                                        |  |  |
| <b>Residence:</b>                                                                   |                                                                                             |  |  |
| <b>City of Residence:</b>                                                           | Tai-Chung Hsien                                                                             |  |  |
| <b>Country of Residence:</b>                                                        | TW                                                                                          |  |  |
| <b>Address-1 of Mailing Address:</b>                                                | No. 10, Lane 35, Sec. 3, Sa-Tien Rd., Ta-Tu Hsiang                                          |  |  |
| <b>Address-2 of Mailing Address:</b>                                                |                                                                                             |  |  |
| <b>City of Mailing Address:</b>                                                     | Tai-Chung Hsien                                                                             |  |  |
| <b>State of Mailing Address:</b>                                                    |                                                                                             |  |  |
| <b>Postal Code of Mailing Address:</b>                                              |                                                                                             |  |  |
| <b>Country of Mailing Address:</b>                                                  | TW                                                                                          |  |  |
| <b>Phone:</b>                                                                       |                                                                                             |  |  |
| <b>Fax:</b>                                                                         |                                                                                             |  |  |
| <b>E-mail:</b>                                                                      |                                                                                             |  |  |
| <u>Inventor 2:</u>                                                                  |                                                                                             |  |  |
| <b>Applicant Authority Type:</b>                                                    | Inventor                                                                                    |  |  |
| <b>Citizenship:</b>                                                                 | TW                                                                                          |  |  |
| <b>Given Name:</b>                                                                  | Jy-Hwang                                                                                    |  |  |
| <b>Family Name:</b>                                                                 | Lin                                                                                         |  |  |
| <b>Residence:</b>                                                                   |                                                                                             |  |  |
| <b>City of Residence:</b>                                                           | Hsin-Chu City                                                                               |  |  |

**Country of Residence:** TW  
**Address-1 of Mailing Address:** 18F-3, No. 13, Lane 175, Wu-Ling Rd.  
**Address-2 of Mailing Address:**  
**City of Mailing Address:** Hsin-Chu City  
**State of Mailing Address:**  
**Postal Code of Mailing Address:**  
**Country of Mailing Address:** TW  
**Phone:**  
**Fax:**  
**E-mail:**

Inventor 3:

**Applicant Authority Type:** Inventor  
**Citizenship:** TW  
**Given Name:** Sheng-Hsiung  
**Family Name:** Yang  
**Residence:**  
**City of Residence:** Hsin-Chu City  
**Country of Residence:** TW  
**Address-1 of Mailing Address:** No. 26, Alley 5, Lane 10, Min-Yu II St.  
**Address-2 of Mailing Address:**  
**City of Mailing Address:** Hsin-Chu City  
**State of Mailing Address:**  
**Postal Code of Mailing Address:**  
**Country of Mailing Address:** TW  
**Phone:**  
**Fax:**  
**E-mail:**

Inventor 4:

**Applicant Authority Type:** Inventor  
**Citizenship:** TW  
**Given Name:** Jim  
**Family Name:** SU  
**Residence:**  
**City of Residence:** Yun-Lin Hsien  
**Country of Residence:** TW  
**Address-1 of Mailing Address:** No. 10, Chung-Cheng Rd., Gang-Dung Tsun, Kou-Hu  
**Address-2 of Mailing Address:** Hsiang  
**City of Mailing Address:** Yun-Lin Hsien  
**State of Mailing Address:**  
**Postal Code of Mailing Address:**

**Country of Mailing Address:** TW

**Phone:**

**Fax:**

**E-mail:**

**Attorney Information:**

practitioner(s) at Customer Number:

027765



as our attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.